



PUMB15-Q

50 V, 100 mA PNP/PNP resistor-equipped double transistor;
R1 = 4.7 k Ω , R2 = 4.7 k Ω

12 August 2025

Product data sheet

1. General description

PNP/PNP double Resistor-Equipped Transistor (RET) in a very small SOT363 (SC-88) Surface-Mounted Device (SMD) plastic package.

NPN/PNP complement: PUMD15-Q

NPN/NPN complement: PUMH15-Q

2. Features and benefits

- 100 mA output current capability
- Built-in bias resistors
- Simplifies circuit design
- Reduces component count
- Reduces pick and place costs
- Qualified according to AEC-Q101 and recommended for use in automotive applications

3. Applications

- Low current peripheral driver
- Control of IC inputs
- Replaces general-purpose transistors in digital applications

4. Quick reference data

Table 1. Quick reference data

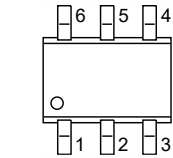
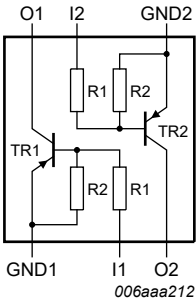
Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Per transistor						
V _{CEO}	collector-emitter voltage	open base	-	-	-50	V
I _O	output current		-	-	-100	mA
R1	bias resistor 1 (input)	[1]	3.3	4.7	6.1	k Ω
R2/R1	bias resistor ratio	[1]	0.8	1	1.2	

[1] See "Section 11: Test information" for resistor calculation and test conditions.

50 V, 100 mA PNP/PNP resistor-equipped double transistor; R1 = 4.7 kΩ, R2 = 4.7 kΩ

5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	GND1	GND (emitter) TR1	 TSSOP6 (SOT363)	 006aaa212
2	I1	input (base) TR1		
3	O2	output (collector) TR2		
4	GND2	GND (emitter) TR2		
5	I2	input (base) TR2		
6	O1	output (collector) TR1		

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PUMB15-Q	TSSOP6	plastic, surface-mounted package; 6 leads; 0.65 mm pitch; 2.1 mm x 1.25 mm x 0.95 mm body	SOT363

7. Marking

Table 4. Marking codes

Type number	Marking code[1]
PUMB15-Q	B%6

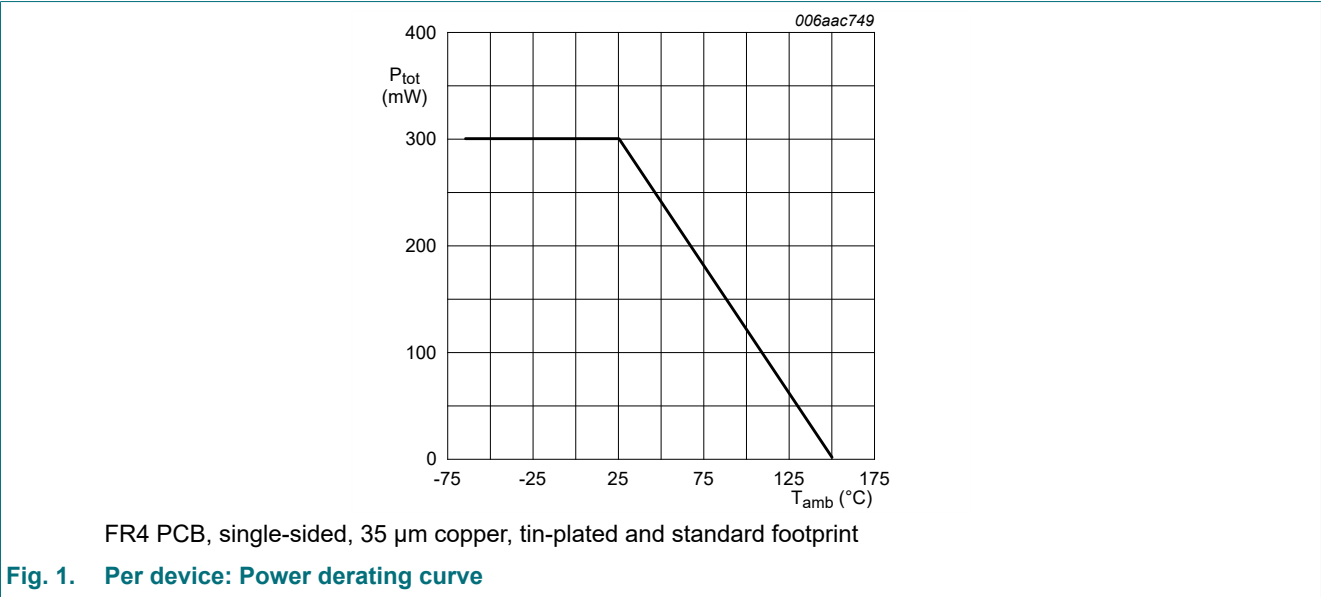
[1] % = placeholder for manufacturing site code

8. Limiting values

Table 5. Limiting values
In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
Per transistor						
V _{CBO}	collector-base voltage	open emitter		-	-50	V
V _{CEO}	collector-emitter voltage	open base		-	-50	V
V _{EBO}	emitter-base voltage	open collector		-	-10	V
V _I	input voltage	positive		-	10	V
		negative		-	-30	V
I _O	output current			-	-100	mA
I _{CM}	peak collector current	t _p ≤ 1 ms; single pulse		-	-100	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	200	mW
Per device						
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	[1]	-	300	mW
T _j	junction temperature			-	150	°C
T _{amb}	ambient temperature			-65	150	°C
T _{stg}	storage temperature			-65	150	°C

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided, 35 μm copper, tin-plated and standard footprint.

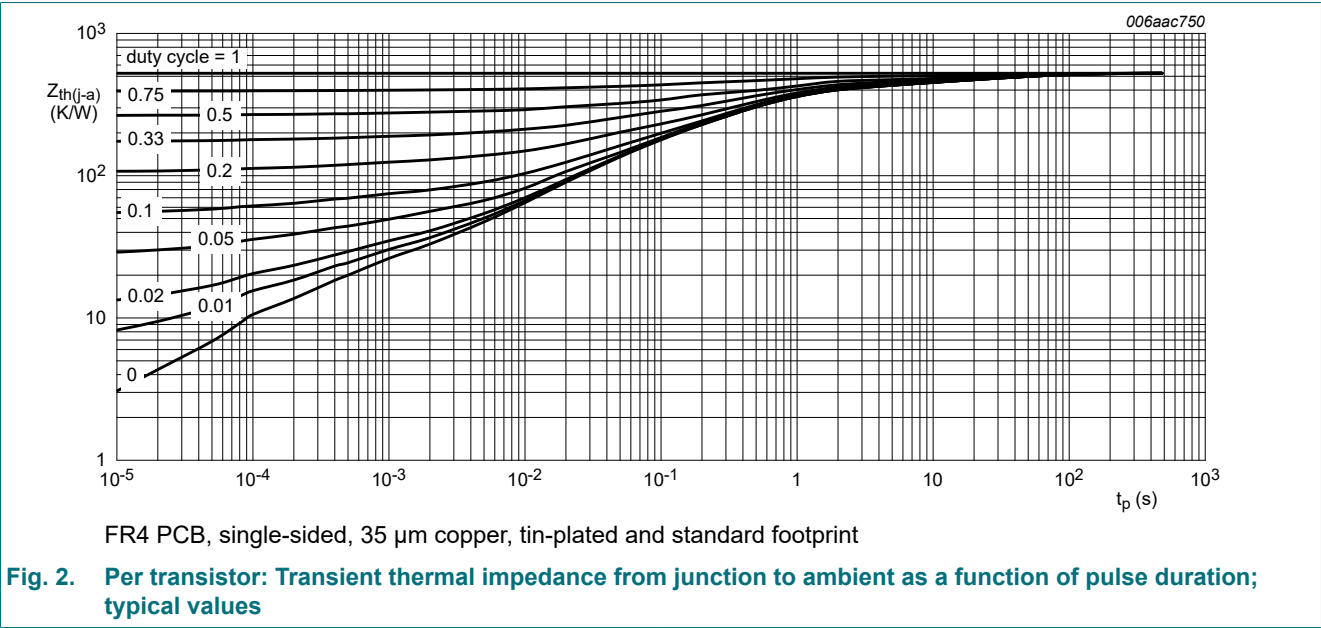


9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
Per transistor							
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]	-	-	625	K/W
Per device							
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]	-	-	417	K/W

[1] Device mounted on an FR4 PCB, single-sided, 35 μm copper, tin-plated and standard footprint.



10. Characteristics

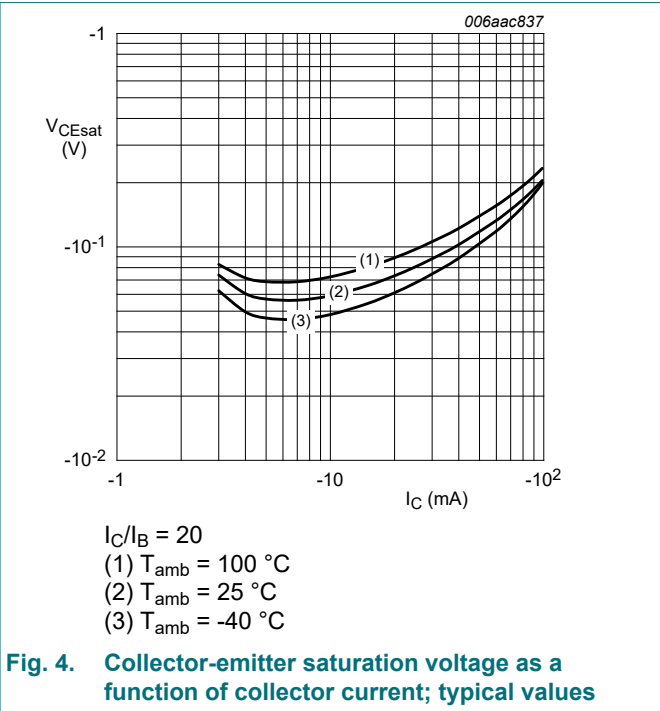
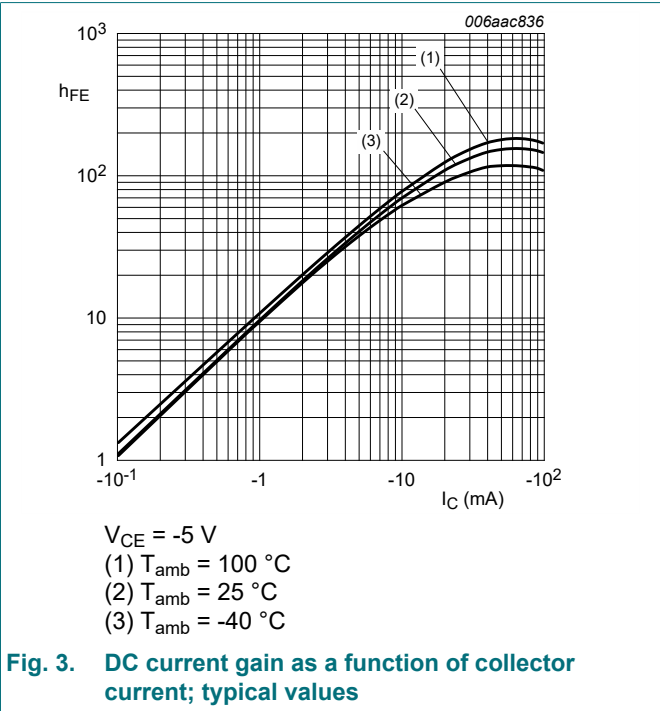
Table 7. Characteristics

T_{amb} = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Per transistor						
V _{(BR)CBO}	collector-base breakdown voltage	I _C = -100 μA; I _E = 0 A	-50	-	-	V
V _{(BR)CEO}	collector-emitter breakdown voltage	I _C = -2 mA; I _B = 0 A	-50	-	-	V
I _{CBO}	collector-base cut-off current	V _{CB} = -50 V; I _E = 0 A	-	-	-100	nA
I _{CEO}	collector-emitter cut-off current	V _{CE} = -30 V; I _B = 0 A	-	-	-100	nA
		V _{CE} = -30 V; I _B = 0 A; T _j = 150 °C	-	-	-5	μA
I _{EBO}	emitter-base cut-off current	V _{EB} = -5 V; I _C = 0 A	-	-	-900	μA
h _{FE}	DC current gain	V _{CE} = -5 V; I _C = -10 mA	30	-	-	
V _{CEsat}	collector-emitter saturation voltage	I _C = -10 mA; I _B = -0.5 mA	-	-	-150	mV
V _{I(off)}	off-state input voltage	V _{CE} = -5 V; I _C = -100 μA	-	-1.1	-0.5	V
V _{I(on)}	on-state input voltage	V _{CE} = -0.3 V; I _C = -20 mA	-2.5	-1.9	-	V
R1	bias resistor 1 (input)	[1]	3.3	4.7	6.1	kΩ
R2/R1	bias resistor ratio	[1]	0.8	1	1.2	
C _c	collector capacitance	V _{CB} = -10 V; I _E = 0 A; i _e = 0 A; f = 1 MHz	-	-	3	pF
f _T	transition frequency	V _{CE} = -5 V; I _C = -10 mA; f = 100 MHz	[2]	180	-	MHz

[1] See "Section 11: Test information" for resistor calculation and test conditions.

[2] Characteristics of built-in transistor



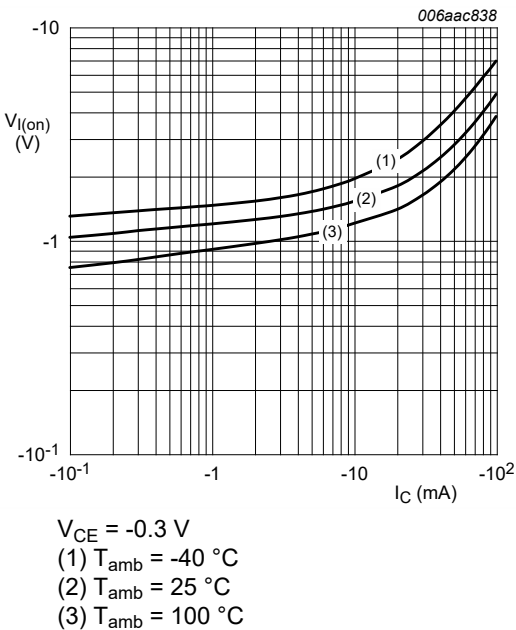


Fig. 5. On-state input voltage as a function of collector current; typical values

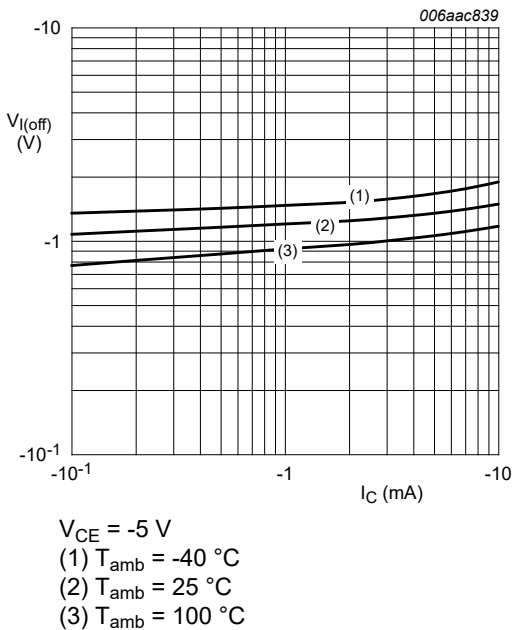


Fig. 6. Off-state input voltage as a function of collector current; typical values

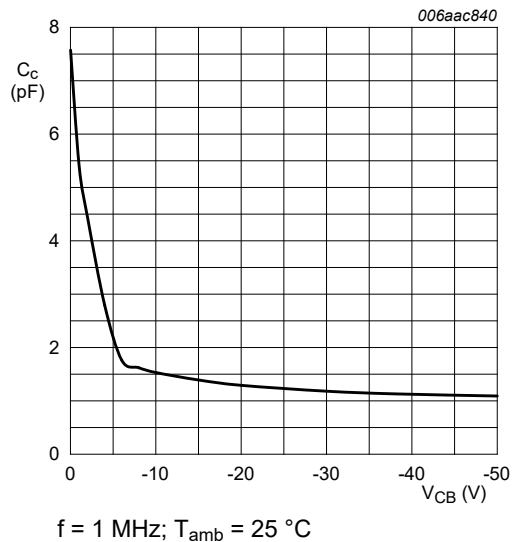


Fig. 7. Collector capacitance as a function of collector-base voltage; typical values

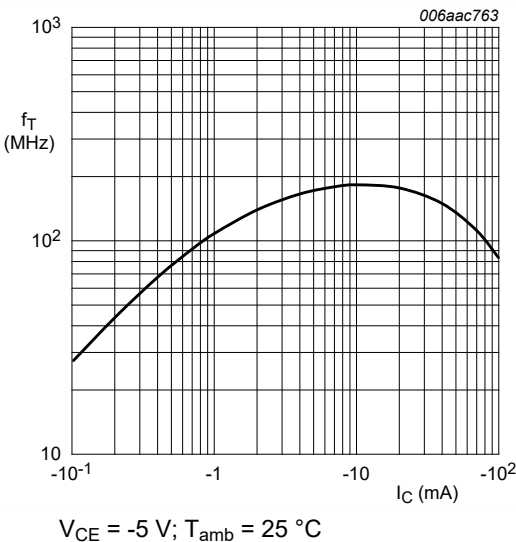


Fig. 8. Transition frequency as a function of collector current; typical values of built-in transistor

11. Test information

Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

Resistor calculation

- Calculation of bias resistor 1 (R1)

$$R_1 = \frac{V(I_2) - V(I_1)}{I_2 - I_1}$$

- Calculation of bias resistor ratio (R2/R1)

$$\frac{R_2}{R_1} = \frac{V(I_4) - V(I_3)}{R_1 \cdot (I_4 - I_3)} - 1$$

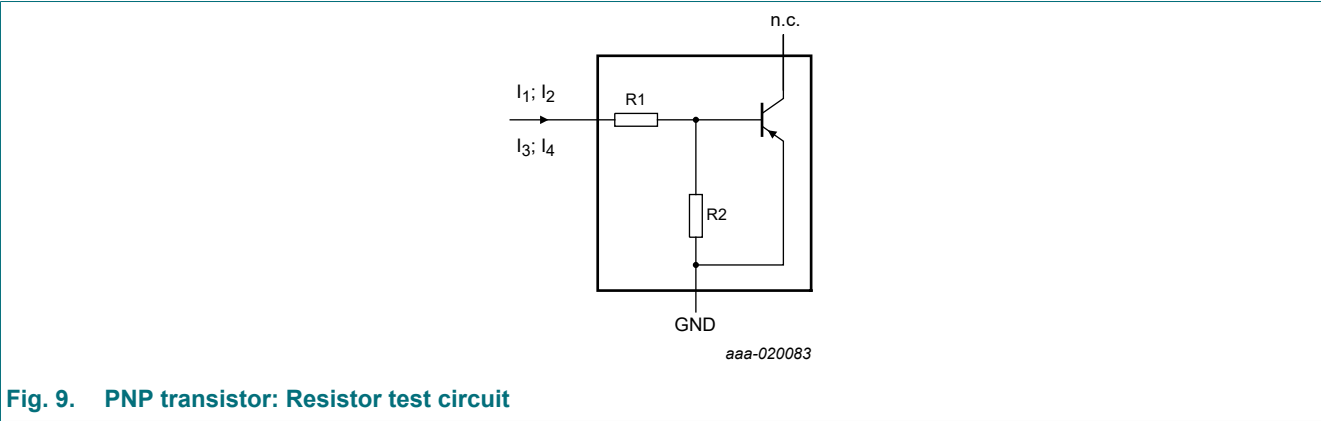


Fig. 9. PNP transistor: Resistor test circuit

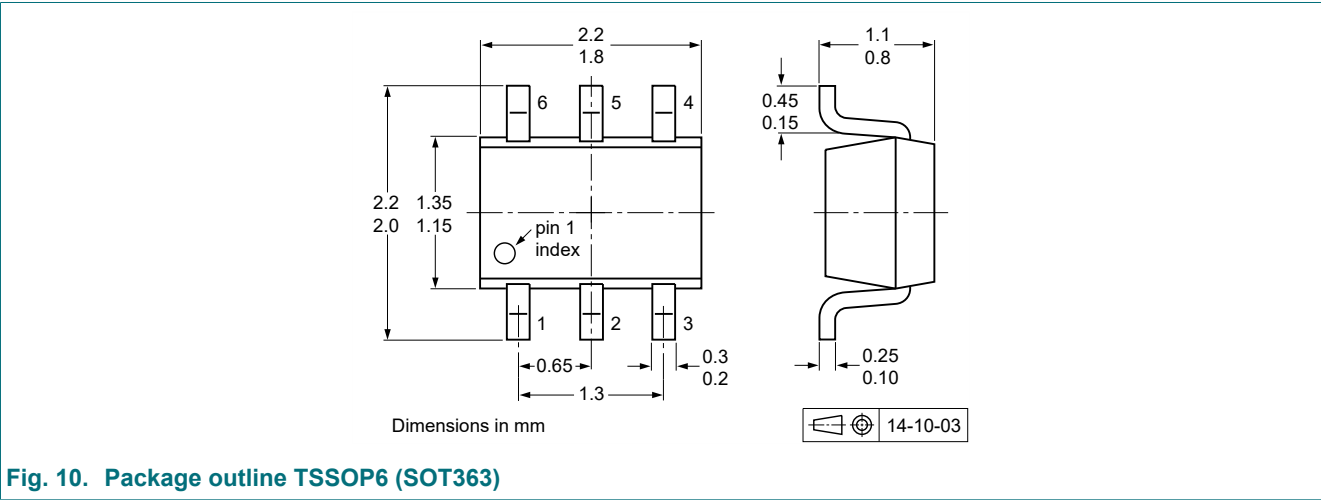
Resistor test conditions

Table 8. Resistor test conditions

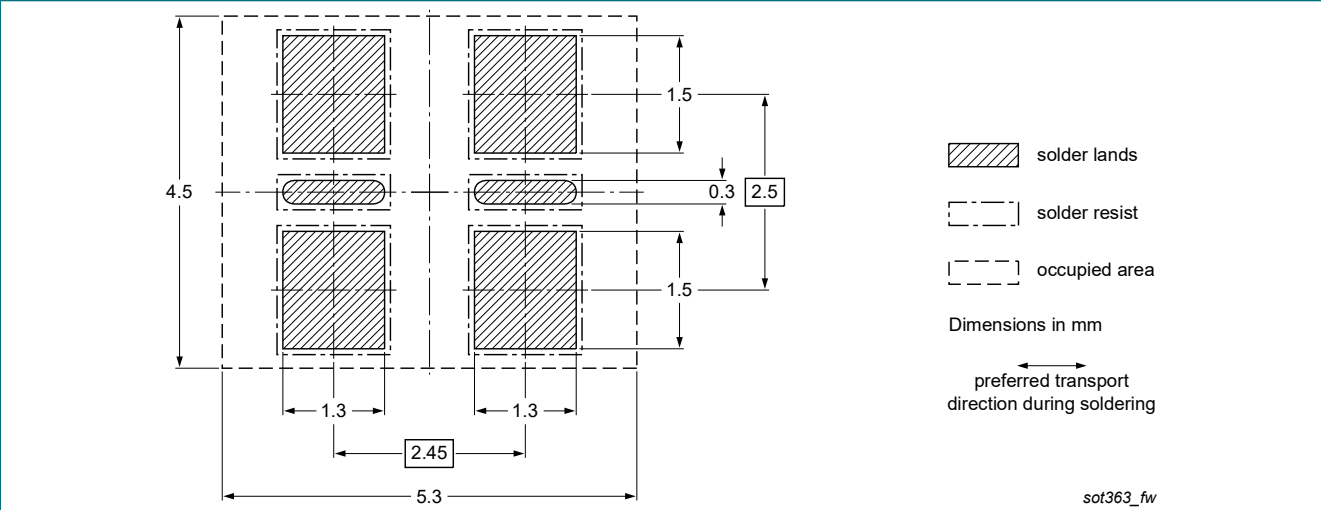
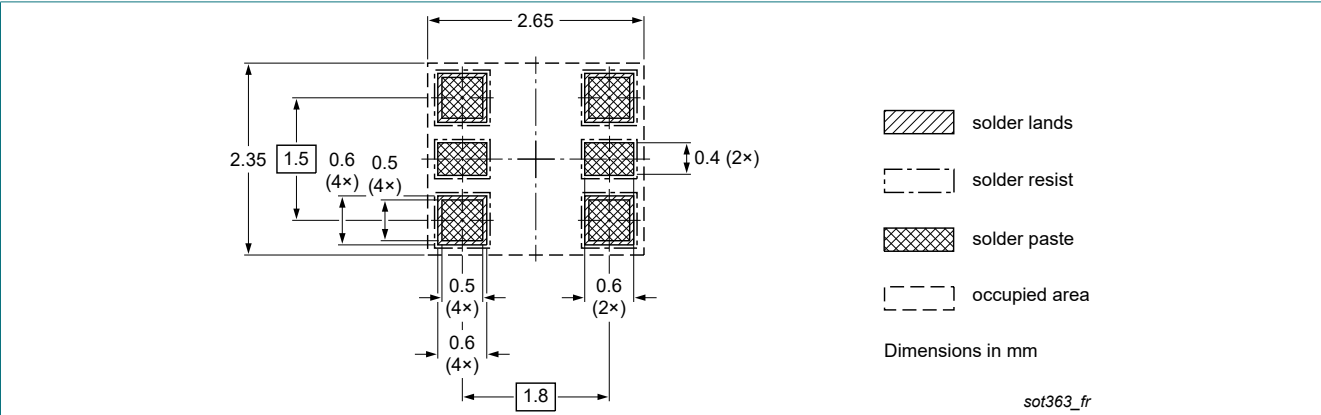
Per transistor; for the PNP transistor with negative polarity

Product	R1 (kΩ)	R2 (kΩ)	Test conditions			
			I1	I2	I3	I4
PUMB15-Q	4.7	4.7	-600 μA	-700 μA	600 μA	700 μA

12. Package outline



13. Soldering



50 V, 100 mA PNP/PNP resistor-equipped double transistor; R1 = 4.7 kΩ, R2 = 4.7 kΩ

14. Revision history

Table 9. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PUMB15-Q v.1	20250812	Product data sheet	-	-

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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- [2] The term 'short data sheet' is explained in section "Definitions".
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